[Method for recording power failure time of a computer system]

Abstract of Disclosure

A method for recording power failure time of a computer system. The computer system includes a power supply for generating a power signal, a memory for recording data, and a processor for processing data. The processor has a power port connected to the power supply for receiving the power signal, an input port for receiving a power good signal, and an output port connected to the memory for outputting the power failure time of the computer system to the memory. The recording method includes writing a power failure time and a check number into the memory when the input port of the processor does not receive the power good signal, andwhen the power signal has dropped below a threshold voltage.

Figures